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contains the **prologue** that defines return reg-. ister type, local variable requirements ...

Flow Graph(CFG). The wavefront scheduler acts on these to form ...

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in the control **flow graph (CFG)** across procedure boundaries to ... ered as a **candidate** for placement in the LC, the energy savings ...

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Predication (also called if-conversion and conditional execution), converts multiple regions of a control **flow graph** into a single region composed of ...

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Optimize Options - Using the GNU Compiler Collection (GCC)

These heuristics are based on the control **flow graph**. Perform branch target register load optimization before **prologue** / epilogue threading. ...

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in the control **flow graph (CFG)** across procedure boundaries to Alternately, one could place the copies in the **prologue** block. of the 'main' procedure. ...

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based scheduling use profiles of control-**flow-graph**. (CFG) ing the **candidate**. As currently implemented, we stop at any ...

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includes the entire control **flow graph** as an outermost containing region. in the real iteration to be moved to a **prologue** of the loop. ...

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High overhead: extra code for loop body, **prologue**, and epilogue On normalized single entry single exit control **flow graph** ...

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optimization, and combined register allocation and **code motion**. The Control **Flow Graph (CFG)** [6] is perhaps the oldest program **graph**. The basis of the ...
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SYNOPSIS

The file name is made by appending .gimple to the source file name. **cfg** Dump the control **flow graph** of each function to a file. ...

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At process block 530, an **inverse graph** of the CFG loop is built. ... Hence, **hoisting** or sinking any **motion candidate instructions** cannot violate data ...

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Each phase consists of a Morel-Rennoise-style code **motion** [20], in which **candidate** statements are moved through the control flow. **graph** in such a way that ...

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Loop-invariant code **motion** is responsible for **hoisting** loop-invariant ... within a control ow **graph** (CFG). However, the previous versions of PRE are based ...

llvm.org/ProjectsWithLLVM/2002-Fall-CS426-SSAPRE.ps - [Similar pages](#)

[PS] c Copyright by Chris Arthur Lattner, 2002

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We traverse the **inverse** call **graph** (the call **graph** computed by the Bottom-Up references, allowing it to **hoist** memory access **instructions** out of loops. ...

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Definition 3.2.1 (Control Flow **Graph**) A control flow **graph** (CFG) of a The **graph** contains for all **instructions** $m \in V$, all **candidate** blocks ...

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of control enters a block only at its first **instruction** and exits only at its last **instruction**. A control flow **graph** (CFG) is a directed **graph** where the ...

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To minimize the stalls related to speculative loads, do not **hoist** flow **graph** (CFG); at this point, **instructions** in basic blocks are ...

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global code **motion**, and. loop invariant **hoisting**. To support **graph** viewing of a CFG, the following low-level primitives are provided:.. val viewStyle ...

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Since data-flow analysis depends upon a correct control-flow **graph** (CFG), we first Also shown is the **inverse** function. It is interesting to compare these ...

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-fsched-spec-load Allow speculative **motion** of some load **instructions**. thus **hoisting** loads out of loops and doing inter-block scheduling needs a ...

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